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PATENT APPLICATION
Attorney's Doc. No. 5485-089

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: 6,710,886 B2
Inventor: Park et al.
Issued: Mar. 23, 2004

For: SEMICONDUCTOR WAFER POSITION DETECTING SYSTEM,
SEMICONDUCTOR DEVICE FABRICATING FACILITY OF USING THE SAME,
AND WAFER POSITION DETECTING METHOD THEREOF

Ser. No.: 10/027,641
Group Art Unit: 2877

Filed: Dec. 19, 2001
Examiner: Hoa Q. Pham

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
OCT 13 2004
of Correction

Enclosed for filing are the following:

- ☒ Request for Certificate of Correction
- ☒ Certificate of Correction Form PTO 1050
- ☒ Return postcard
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,
MARGER JOHNSON & McCOLLOM, P.C.

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22313-1450

Date: October 4, 2004

Susan Kral

OCT 15 2004



PATENT NO. 6,710,886
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REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefor, the following error in the original Letters Patent has been noted:

Column 8, line 2, "plate 6 in order" should read --plate 46 in order --.

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,
MARGER JOHNSON & McCOLLOM, P.C.

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,710,886 *B2*

DATED : Mar. 23, 2004

INVENTOR(S) : Park et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, line 2, "plate 6 in order" should read --plate 46 in order --.

MAILING ADDRESS OF SENDER:
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PATENT NO. 6,710,886 *B2*

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